

## Description

The DGU5020GR is 500 V IGBT with Zener diodes and gate resistors, and achieves an ignition coil drive circuit without an external clamped circuit. The IGBT has low saturation characteristic, and can improve the efficiency of the circuit.

#### **Features**

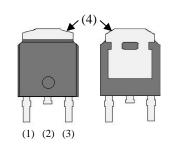
- AEC-Q101 Qualified
- Bare Lead Frame: Pb-free (RoHS Compliant)
- Built-in Zener Diodes
- Built-in Gate Resistors
- Low Saturation Voltage
- V<sub>(BR)CES</sub> ------ 500 V I<sub>C</sub> ------20 A
- $V_{CE(SAT)}$  ------ 1.15 V typ. ( $V_{GE} = 4.5$  V,  $I_C = 10$  A)

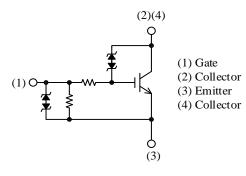
#### Applications

• Ignition Coil Driver Circuits

## **Packages**





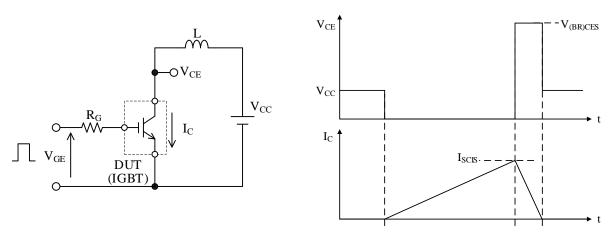


Not to scale

### **Absolute Maximum Ratings**

Parameter	Symbol	Conditions	Rating	Unit
Collector-to-Emitter Voltage	V <sub>CE</sub>		V <sub>(BR)CES</sub>	V
Gate-to-Emitter Voltage	V <sub>GE</sub>		±10	V
Continuous Collector Current	I <sub>C</sub>	$T_C = 25 \ ^{\circ}C$	20	А
Power Dissipation	PD	$T_C = 25 \ ^{\circ}C$	172	W
Self-clamped Inductive Switching Energy	E <sub>SCIS</sub>	See Figure 1 and Equation (1).	280	mJ
Self-clamped Inductive Switching Current	I <sub>SCIS</sub>	$V_{CC} = 14 V, V_{GE} = 5 V, L = 1.36 mH, R_G = 1 k\Omega$	20	А
Reverse Avalanche Energy	E <sub>AS(R)</sub>	L = 6 mH	2000	mJ
Operating Junction Temperature	TJ		-40 to 175	°C
Storage Temperature	T <sub>STG</sub>		-40 to 175	°C





(a) Test Circuit

(b) Waveform

Figure 1. Self-clamped Inductive Switching Energy Test

$$E_{SCIS} = \frac{1}{2} \times L \times I_{SCIS}^{2} \times \frac{V_{(BR)CES}}{V_{(BR)CES} - V_{CC}}$$

(1)

### **Electrical Characteristics**

Parameter Symbol Conditions Min. Unit Тур. Max. Collector-to-Emitter Breakdown V<sub>(BR)CES</sub> V  $I_{C} = 2 \text{ mA}, V_{GE} = 0 \text{ V}$ 475 500 525 Voltage Gate-to-Emitter Breakdown V<sub>(BR)GES</sub>  $I_G = \pm 1 \text{ mA}, V_{CE} = 0 \text{ V}$  $\pm 10.0$ V  $\pm 11.5$ ±13.0 Voltage Collector-to-Emitter Leakage  $V_{CE} = 400 \text{ V}, V_{GE} = 0 \text{ V}$ 100 ICES μA Current Emitter-to-Collector Leakage IECS  $V_{EC} = 24 V$ 1.0 mА Current Gate-to-Emitter Leakage Current  $V_{GE} = \pm 5 V$ IGES  $\pm 89$  $\pm 106$ ±132 μA Gate Threshold Voltage V<sub>GE(TH)</sub>  $V_{CE} = 10 \text{ V}, I_C = 1 \text{ mA}$ 1.40 1.75 2.10 V  $V_{GE} = 3.5 V$ . V 1.20 1.45  $I_{C} = 10 A$  $V_{GE} = 4.5 V$ , 1.40 V 1.15  $I_{C} = 10 A$  $T_J = 25 \ ^{\circ}C$  $V_{GE} = 4.5 V$ , V 1.30 1.60  $I_{\rm C} = 15 \, {\rm A}$  $V_{GE} = 4.5 V$ , 1.45 1.75 V  $I_C = 20 \text{ A}$ Collector-to-Emitter Saturation V<sub>CE(SAT)</sub> Voltage  $V_{GE} = 3.5 V$ , V 1.20 1.60  $I_{C} = 10 A$  $V_{GE} = 4.5 V$ , V 1.20 1.45  $I_{C} = 10 \text{ A}$  $T_J = 150 \ ^\circ C$  $V_{GE} = 4.5 V$ , V 1.35 1.85  $I_{C} = 15 \text{ A}$  $V_{GE} = 4.5 V$ , 1.65 2.20 V  $I_{C} = 20 A$ Input Capacitance  $C_{\text{ies}}$ 1900 pF  $V_{CE} = 10 V$ , **Output Capacitance** Coes  $V_{GE} = 0 V,$ 460 pF f = 1.0 MHz**Reverse Transfer Capacitance** Cres 160 pF Resistive load, Turn-on Delay Time 1.3 t<sub>d(ON)</sub> μs  $V_{CE} = 14 \text{ V}, V_{GE} = 5 \text{ V},$ Rise Time  $R_G = 1 k\Omega, R_L = 1 \Omega;$ 3.8 tr μs see Figure 3 Turn-off Delay Time Inductive load, 13.5  $t_{d(OFF)}$ μs  $V_{CE} = 300 V$ ,  $I_C = 10 A, V_{GE} = 5 V,$ Fall Time 2.7 μs  $t_{\mathrm{f}}$  $R_G = 1 k\Omega$ , L = 2 mH; see Figure 4  $R_{\underline{G(INT)}}$ Internal Series Gate Resistor<sup>(1)</sup> 70 Ω Internal Gate-to-Emitter Resistor (1) R<sub>GE(INT)</sub>  $61.1^{(2)}$  $T_J = -40$  to 175 °C 47.0 kΩ 37.6

Unless otherwise specified,  $T_A = 25$  °C.

<sup>(2)</sup> Guaranteed by design.

<sup>&</sup>lt;sup>(1)</sup> See Figure 2

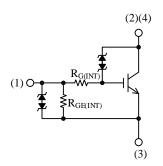


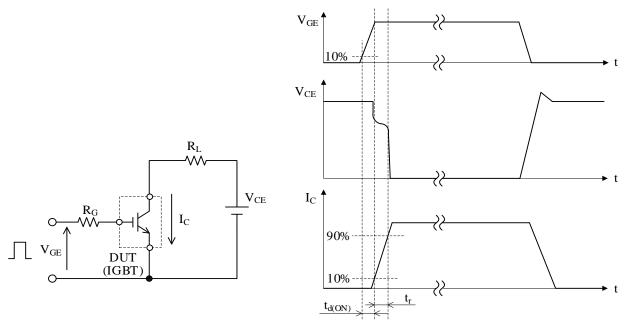
Figure 2. Internal Gate Resistor

## **Thermal Characteristics**

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Thermal Resistance (Junction-to-Case)	$R_{\theta JC}$				0.87	°C/W

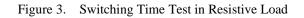
# **Mechanical Characteristics**

Parameter	Conditions	Min.	Тур.	Max.	Unit
Package Weight		_	0.32		g



(a) Test Circuit

(b) Waveform



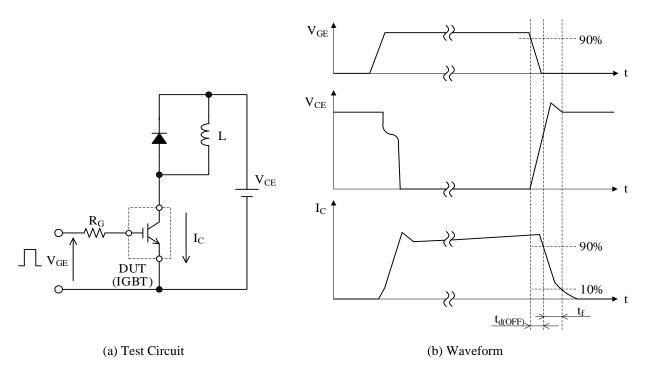


Figure 4. Switching Time Test in Inductive Load

#### **Derating Curves**

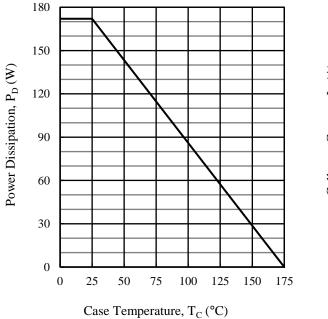


Figure 5. P<sub>D</sub> vs. T<sub>C</sub>

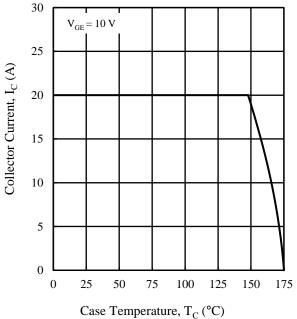


Figure 6.  $I_C$  vs.  $T_C$  ( $V_{GE} = 5$  V)

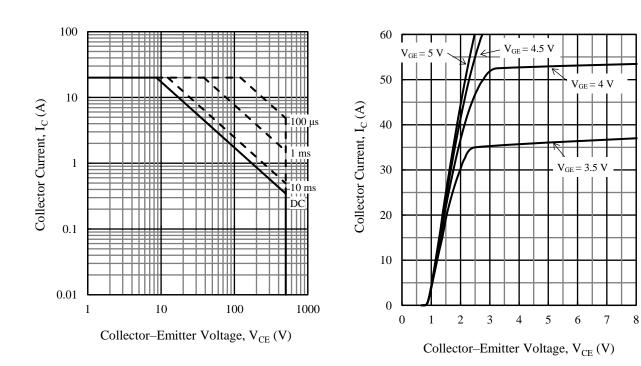
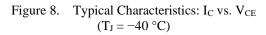


Figure 7. Safe Operating Area



**Typical Characteristic Curves** 

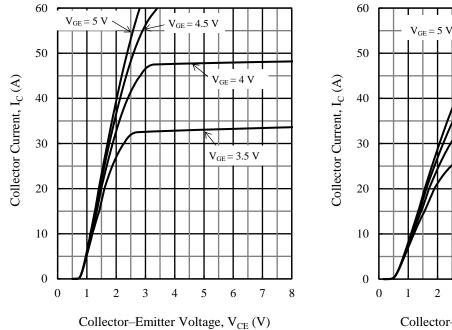


Figure 9. Typical Characteristics:  $I_{C}$  vs.  $V_{CE}$   $(T_{J}=25\ ^{\circ}C)$ 

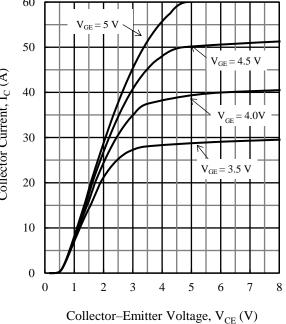


Figure 10. Typical Characteristics:  $I_{C}$  vs.  $V_{CE}$   $(T_{J}$  = 175  $^{\circ}C)$ 

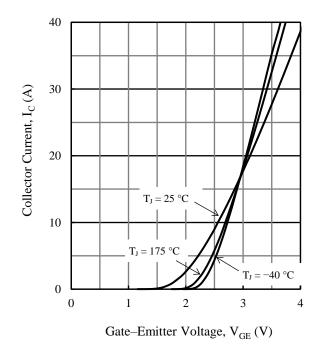


Figure 11. Typical Characteristics:  $I_C$  vs.  $V_{GE}$ ( $V_{CE} = 5 V$ )

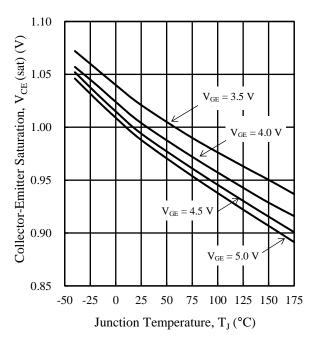


Figure 12. Typical Characteristics:  $V_{CE(SAT)}$  vs.  $T_J$   $(I_C = 6 \text{ A})$ 

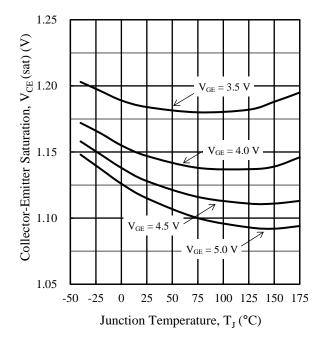


Figure 13. Typical Characteristics:  $V_{CE(SAT)}$  vs.  $T_J$  (I<sub>C</sub> = 10 A)

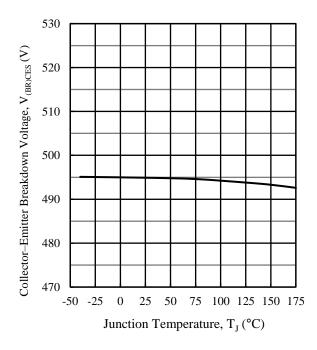


Figure 15. Typical Characteristics:  $V_{(BR)CES}$  vs.  $T_J$ ( $V_{GE} = 0$  V,  $I_C = 2$  mA)

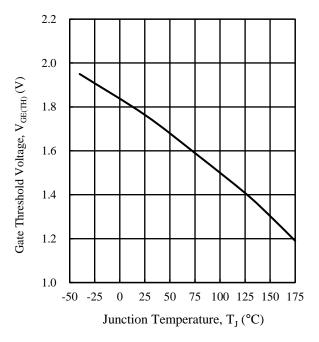


Figure 14. Typical Characteristics:  $V_{GE(TH)}$  vs.  $T_J$ ( $V_{CE} = 10$  V,  $I_C = 1$  mA)

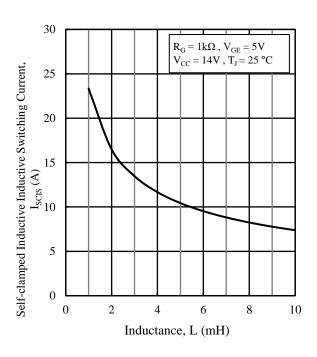
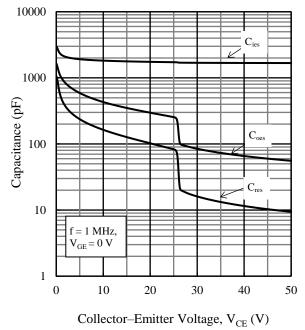
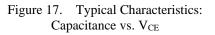
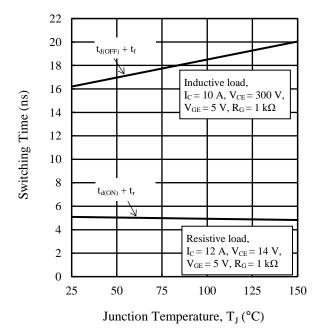
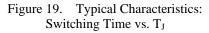


Figure 16. Typical Characteristics: ISCIS vs. L









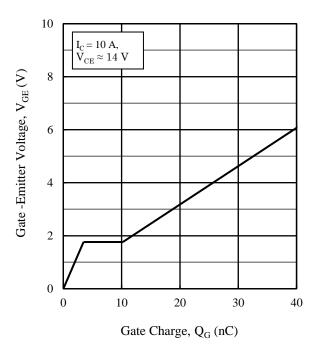


Figure 18. Typical Characteristics: VGE vs. QG

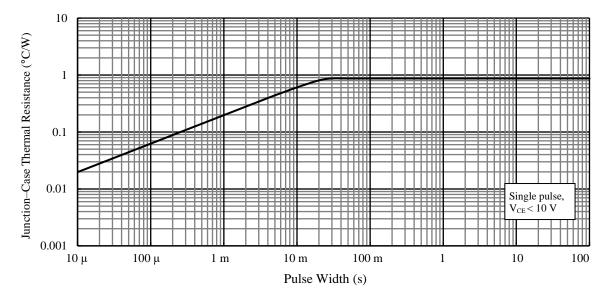
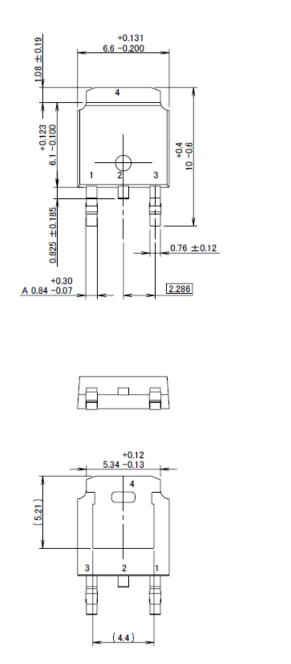
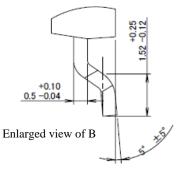


Figure 20. Transient Thermal Resistance Characteristics

#### **Physical Dimensions**

• TO252-2L Package





+0.08 2.3 -0.10

+0.08

B

 $0.0635 \pm 0.0635$ 

0.5 -0.04

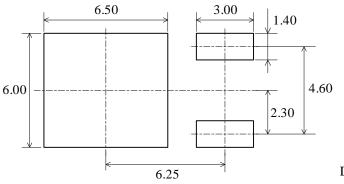
#### NOTES:

- Dimensions in millimeters
- All the dimensions exclude mold flashes, protrusions, and gate burrs.
- Bare lead frame: Pb-free (RoHS compliant)
- Moisture Sensitivity Level 1 (MSL 1)
- When soldering the products, it is required to minimize the working time within the following limits: Reflow

Preheat: 150 °C to 200 °C / 60 s to 120 s Solder heating: 255 °C / 30 s, 3 times (260 °C peak)

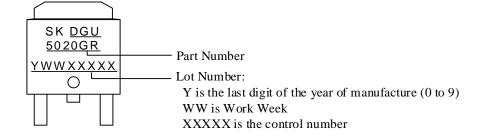
Soldering iron: 350 °C / 3.5 s, 1 time

#### • TO252-2L Land Pattern Example



Dimensions in millimeters

# **Marking Diagram**



#### **Important Notes**

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